

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,977,214 B2
APPLICATION NO. : 09/943763
DATED : December 20, 2005
INVENTOR(S) : David J. Corisis

Page 1 of 5

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

In column 2 below (57) **ABSTRACT**

change “28 Claims, 4 Drawing Sheets” to --27 Claims, 4 Drawing Sheets--

In the drawings:

In FIG. 3,

replace informal drawing of FIG. 3 with formal drawing of FIG. 3

In FIG. 4,

replace informal drawing of FIG. 4 with formal drawing of FIG. 4

In FIG. 5,

replace informal drawing of FIG. 5 with formal drawing of FIG. 5

In the specification:

COLUMN 3, LINE 17,

after “on” and before “leadframe” insert --the--

COLUMN 3, LINES 21-22,

delete “These tabs may be formed on the sides of the paddle along which from the paddle support bars.”

In the claims:

COLUMN 6, LINE 15,

change “arid” to --and--
delete “28. A method of bonding a portion of a wire to a portion of a lead of a leadframe having a semiconductor device having a plurality of bond pads attached to a portion of a die mount paddle of a leadframe, said method comprising:

COLUMN 12, LINES 13-25,

locating said strip of leadframes on a lower clamp member of a wire bonding device having an upper clamp member overlying a portion of at least one lead and a portion of at least one integral clamping tab of said strip of leadframes; and

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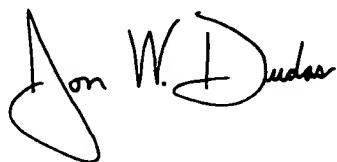
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attaching at least one bond wire to said plurality of bond pads of said semiconductor device and said portions of said at least two inner leads."

Signed and Sealed this
Fifteenth Day of January, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office

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Replace FIG. 3 with the following amended figure:

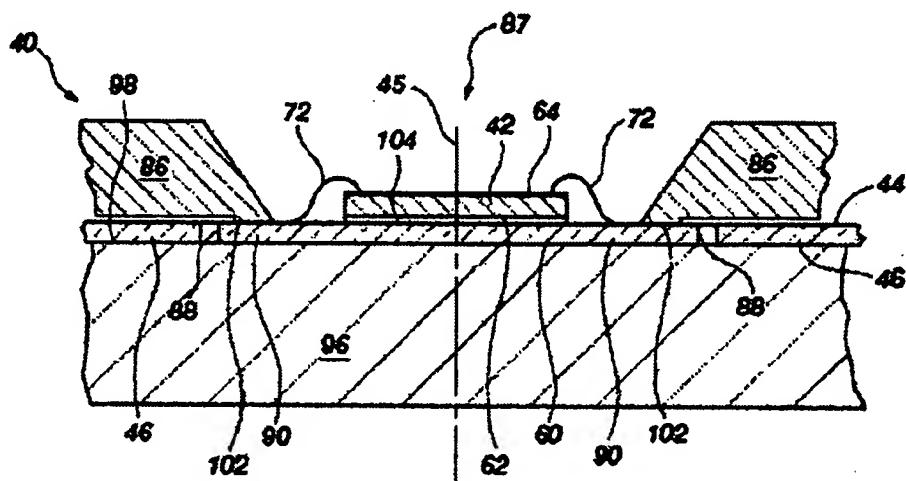


Fig. 3

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Replace FIG. 4 with the following amended figure:

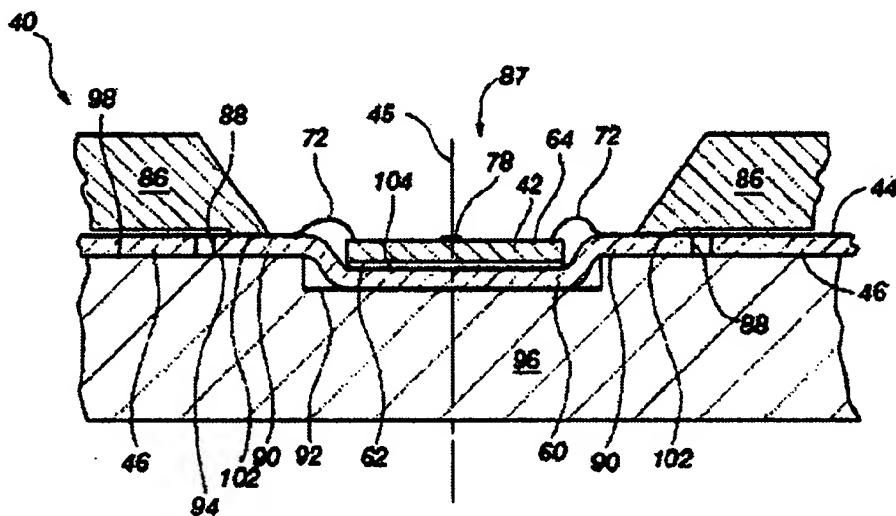


Fig. 4

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Replace FIG. 5 with the following amended figure:

